

PTO/SB/08a (03-09)

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Substitute for form 1449A/PTO			<b>Complete if Known</b>		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)			Application Number	10/586,788	
			Filing Date	September 2, 2008	
			First Named Inventor	Gabric, et al.	
			Art Unit	2893	
			Examiner Name	Nikolay K. Yushin	
Sheet	1	of	1	Attorney Docket Number	INF 2006 VJ 33543 US

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code <sup>2</sup> (if known)			
	1	US-5,759,913	06-02-1998	Fulford, Jr., et al.	
	2	US-5,792,706	08-11-1998	Michael, et al.	
	3	US-6,208,015 B1	03-27-2001	Bandyopadhyay, et al.	
	4	US-2005/0074961 A1	04-07-2005	Beyer, et al.	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				

Examiner Signature	/Nikolay Yushin/	Date Considered	04/12/2010
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		Number - Kind Code <sup>2</sup> (if known)			
	1	US-5,407,860	04-18-1995	Stoltz, <i>et al.</i>	
	2	US-5,837,618	11-17-1998	Avanzino, <i>et al.</i>	
	3	US-6,423,629 B1	07-23-2002	Ahn, <i>et al.</i>	
	4	US-6,440,839 B1	08-27-2002	Partovi, <i>et al.</i>	
	5	US-6,524,948 B2	02-25-2003	Tamaoka, <i>et al.</i>	
	6	US-2003/0183940 A1	10-02-2003	Noguchi, <i>et al.</i>	
	7	US-2004/0124446 A1	07-01-2004	Borger, <i>et al.</i>	
	8	US-2005/0012219 A1	01-20-2005	Liou	
	9	US-6,861,332 B2	03-01-2005	Park, <i>et al.</i>	
	10	US-2005/0067673 A1	03-31-2005	Geffken, <i>et al.</i>	
	11	US-2006/0057835 A1	03-16-2006	Anderson, <i>et al.</i>	
	12	US-7,071,091 B2	07-04-2006	Clarke, <i>et al.</i>	

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		Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (if known)				
	13	WO 02/095820 A2	11-28-2002	Infineon Technologies AG, <i>et al.</i>		
	14	DE 102 46 830 A1	02-12-2004	Infineon Technologies AG, <i>et al.</i>		
	15	DE 101 40 754 A1	03-27-2003	Infineon Technologies AG, <i>et al.</i>		
	16	DE 103 41 544 A1	04-07-2005	Infineon Technologies AG, <i>et al.</i>		

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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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### NON PATENT LITERATURE DOCUMENTS

Examiner  
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Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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17 ARNAL, V., *et al.*, "Integration of a 3 Level Cu-SiO<sub>2</sub> Air Gap Interconnect for Sub 0.1 micron CMOS Technologies," IEEE, 2001, 3 pages.

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